

**IN THE SPECIFICATION**

Please amend the Specification as follows:

Page 2, line 4, under the Related Patent Applications section, the paragraph starting there-at was amended from the prior amendment as follows:

"This non-provisional United States (U.S.) patent application claims the benefit of and is a continuation application of U.S. Patent Application No. 09/469,961 filed on December 21, 1999 by inventors Edward T. Grochowski, et al., entitled "REPLAY MECHANISM FOR CORRECTING SOFT ERRORS", now U.S. Pat. No. 6,625,756; which claims the benefit of and is a continuation-in-part of U.S. Application Serial No. 08/994,503, entitled "PROCESSOR PIPELINE INCLUDING BACKEND REPLAY", which was filed on December 19, 1997, now U.S. Pat. No. 6,047,370."

Page 19, line 20, after the preliminary amendment please further amend the paragraph beginning there-at and continuing to page 20, line 8 as follows:

"For one embodiment of computer system 100, a recovery routine is provided through non-volatile memory 130 [[140]]. Check unit 160 may trigger a machine check that invokes a firmware-based error handling routine. For this embodiment, processor 110 may access an error handling routine when check unit 160 signals an error and replay unit 170 fails to correct it after a specified number of tries. One embodiment of a firmware recovery mechanism operates in conjunction with parity protected storage locations. When replay fails to correct a mismatch in execution results, a recovery routine is implemented to read parity bits associated with the storage structures to locate the error. The storage location that produces the error

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may be updated with data from the execution core that does not display any parity errors. A firmware based mechanism for processing soft errors is described in U.S. Patent Application Serial No. 09/469,963, entitled "Firmware Mechanism for Correcting Soft Errors" and filed on even date herewith."